

INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>	Docket Number (Optional) END919980110	Application Number
	Applicant(s) Jimarez et al.	
	Filing Date	Group Art Unit

09/18/01
 02/13/01
 U.S. PAT. & TM. OFF.
 PHOTOCOPIED

U.S. PATENT DOCUMENTS							
*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILED DATE IF APPROPRIATE
NB		4,376,505	3/1983	Wojcik			
NB		5,060,844	10/1991	Behun et al.			
NB		5,147,084	9/1992	Behun et al.			
NB		5,616,520	4/1997	Nishiuma et al.			
NB		5,641,113	6/1997	Somaki et al.			
NB		5,656,862	8/1997	Papathomas et al.			
NB		5,751,068	5/1998	McMahon et al.			
NB		5,773,889	6/1998	Love et al.			

FOREIGN PATENT DOCUMENTS								
	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO

OTHER DOCUMENTS <i>(Including Author, Title, Date, Pertinent Pages, Etc.)</i>		
NB		IBM Technical Disclosure Bulletin, Vol. 32, No. 10B, March 1990, ENCAPSULATED SOLDER JOINT FOR CHIP MOUNTING, page 480.
NB		IBM Technical Disclosure Bulletin, Vol. 32, No. 6A, November 1989, IMPROVED C4 RELIABILITY USING LOW MODULUS DIELECTRIC LAYER, pages 315-316.

EXAMINER Nema Berezny	DATE CONSIDERED 5-15-02
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10811 U.S. PTO
09/782471
02/12/01

*EXAMINER
INITIAL

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

Topper et al., WAFER LEVEL PACKAGE USING DOUBLE BALLS, 2000 International Symposium on Advanced Packaging Materials, pages 198-200.

nb

EXAMINER

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